ON TAT INDUSTRIAL COMPANY

SPECIFICATION FOR LCD MODULE

Customer	· :						
Product Model:		KD500	KD50G21-40NT-A1				
Sample co	ode:						
Designed by	Designed by Chec			Approved by			
Final Approval by Customer							
LCM Mac	hinery OK		LCM O	K			
Checked By			NG, P	roblem survey:			
LCM Disp	olay OK						
Checked By		Approve	ed By				

^{**}The specification of "TBD" should refer to the measured value of sample . If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.

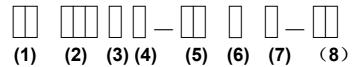
Revision History

Version	Contents	Date	Note
Α	Original	2011. 07. 21	

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1 Numbering System



No	Definition	Specifications
(1)	TFT LCM Productor No.	On Tat Industrial Company
(2)	Display monitor opposite angle line size	Unit :mm or mmm (size <10 inch: takes two integers; size >=10 inch: takes three integers)
(3)	Productor Types	D Digital photo frame / DVD GGPS M MP PMobil-Phone
(4)	Productor Development Series No.	By two figures characters expression from 01 to 99
(5)	Interface PIN Number	By two figures characters expression from 01 to 99
(6)	With Touch Panel Or Not	TWith T/P; NWithout T/P
(7)	LCD Type	AAUO; MCMO; CCPT; PPVI; LLG; WWintek; HHSD; TTianma; YHydis; IHitach; SSharp。。
(8)	Productor Development edition No.	By The English litters : A 1~ Z9
-	All Bights Beconved	1/25 DEV/A

2 Scope

This specification applies to the TFT LCD module which is designed and manufactured by LCM Factory of On Tat Industrial Company

3 Normative Reference

GB/T4619-1996 《 Liquid Crystal Display Test Method》

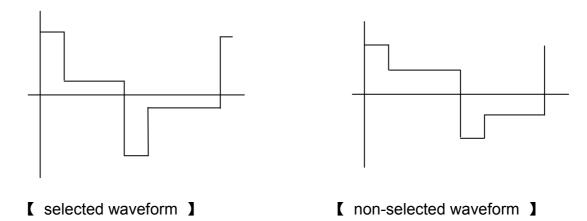
GB/T2424 《Basic environmental Testing Procedures for Electric and Electronic Products.》

GB/T2423 《Basic Testing Procedures for Electric and Electronic Products》 IEC61747-1 《SIXTH PARTGB2828`2829-87《National Standard of PRC》

4 Definitions

4.1 Definitions of Vop

The definitions of threshold voltage Vth,1 Vth2 thef ollowing typical waveforms are applied on liquidc rystal by the methodo f equalized voltage for each duty andb ias.



① Vth1: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of selected waveform

(f_f=80Hz,
$$\Phi$$
=10° θ =270° at 25°C)

② Vth2: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of non-selected waveform

$$(f_f=80Hz, \Phi=10^{\circ} \theta=270^{\circ} at 25^{\circ}C)$$

③ Vop: (Vth1(50%)+Vth2(50%))/2 (f_f =80Hz, Φ =10° θ =270° at 25°C)

4.2 Definition of Response Time Tr. Td

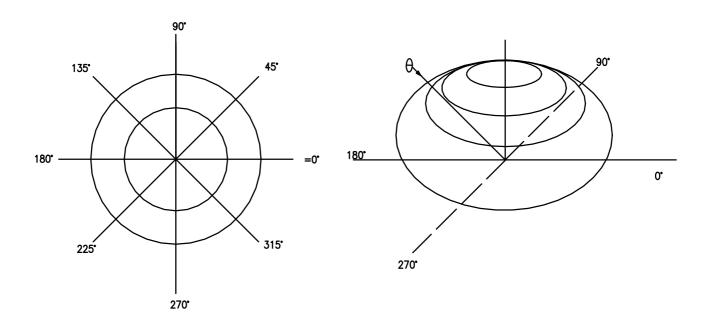
- ①Tr: The time required which the brightness of segment becomes 10% from 100% when waveform is switched to selected one from non-selected one. (f_f =80Hz, Φ =10° θ =270°at 25°C)
- ②Td: The time required which the brightness of segment becomes 90% from 10% when waveform is switched to selected one from selected one. (f_f =80Hz, Φ=10°θ=270°at 25°C)

4.3 Definition of Contrast Ratio Cr

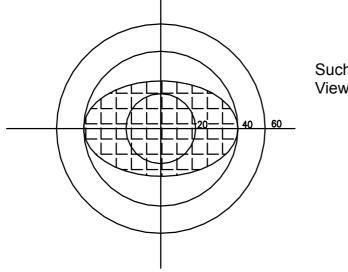
Cr=A/B

- ① A: Segments brightness in case of non-selected waveform
- ② B: Segments brightness in case of selected waveform

4.4 Definition of Angle and Viewing Range



Angular Graph: Constrast Ratio



Such as: Viewing Angle Range: 80(Cr>2) Horizontal 70(Cr>2) Vertical

5 Technology Specifications

5.1 Feature

This single-display module is suitable for use in Multidedia Player products.

The LCD adopts one backlight with High brightness 12-lamps white LED.

- 1) Construction: 5.0 a -Si color TFT-LCD, White LED backlight.
- 2) LCD:
 - 2.1 Amorphous-TFT 5.0 inch display, transmissive, normally white type.
 - 2.2 800(RGB) × 480 dots Matrix.
 - 2.3 Narrow-contact ledge technique.
 - **2.4 LCD Driver IC: HX8264D** \times 1 and HX8664B \times 1.
- 3) Low cross talk by frame rate modulation.
- 4) RGB interface.
- 5) Video signal interface: Parallel RGB or serial.

5.2 Mechanical Specifications

Item	Specifications	Unit
Dimensional outline	120.70(W) ×75.8(H)×3.10 Max(T)	mm
Active area	108.0(W) ×64.8 (H)	mm
Pixel size	135(W) ×135(H)	um
Resolution	800(RGB) × 480	pixel
Luminance for LCM	200	cd/m ²

5.3 Absolute Max. Rating

Item	Symbol	Value			Unit	Remark
item		Min	typ	Max	Offic	Remark
Logic power supply	DVDD	3.0	3.3	3.6	V	
Driver power supply	AVDD	11	12	13	V	
Input high voltage	V _{IH}	0.7DVDD	-	DVDD	V	
Input low voltage	V_{IL}	0	-	0.3DVDD	V	
Operating temperature	T _{OPR}	-20		+7 0	${\mathbb C}$	
Storage temperature	T _{STG}	-30		+8 0	$^{\circ}$	

5.4 Electrical Characteristics (VSS=0V,Ta=-20 to 70°C)

↓ OKB/S

Parameter	Symbol	Applicable	Condition		Spec.		Unit
Parameter	Syllibol	pın		Min.	Тур.	Max.	Ollic
Input H voltage	V _{IH}	All input pins ⁽²⁾	-	0.7VDD	-	VDD	
Input L voltage	V_{IL}	All input pins ⁽²⁾	-	VSS	-	0.3VDD	
Input H voltage	V_{IH}	/XAO		0.8VDD	-	VDD	V
Input L voltage	V_{IL}	/XAO		VSS	-	0.2VDD	V
Output H voltage	V_{OH}	STV1,2	I _{OH} =40μA	VDD-0.4	-	VDD	
Output L voltage	V _{OL}	STV1,2	I _{OL} =40µA	VSS		VSS+0.4	
Output H resistance	R _{OH}	OUT0~ OUT961	V _{OUT} = VGH-0.5V	-		1000	Ω
Output L resistance	R _{OL}	OUT0~ OUT961	V _{OUT} = VEE+0.5V	-	40	1000	Ω
Input leakage current	I _{IN}	Note ⁽²⁾	-	-5.0	\sim	+5.0	μA
Pull high resistance	R _{PU}	/XAO,MODE, SEL	VDD=3.3V, TA=25C	70	160	400	kΩ
VGH Power consumption	I _{VGH}	-	Note ⁽¹⁾	%) <u>-</u>	\wedge	200	
VDD Power consumption	I _{VDD}	-		-6	7/	200	μA

Note: (1) Power consumption in the following condition: Output no load, VGH=20V, VEE=-8V, VDD=3.0V, V_{IH} =VDD, V_{IL} =VSS, F_{CPV} =50KHz, $OE=V_{IL}$, /XAO= V_{IH} .

(2)All input pins except /XAO, MODE, SEL

Parameter	Symbol	Condition		Unit		
Faranietei	Symbol	Condition	Min.	Тур.	Max.	Oilit
CPV period	t _{CPV}	-	5	-	-	
CPV pulse width	t _{CPVH} , t _{CPVL}	50% duty cycle	2.5	-	-	
OE pulse width	t _{woe}	-	1	-	-	
/XAO pulse width	t _{WXAO}	-	30	-	-	
Data setup time	t _{su}	-	0.3	-	-	ш
Data hold time	t _{HD}	-	0.3	- 4	(-	μs
CPV to output delay time	t _{PD1}	CL=200pF	-	- /	0.9	'
Start pulse output delay time	t _{PD2}	Loading=20pF	-	0-69	0.5	
OE to output delay time	t _{PD3}	CL=200pF	- ^	X-\((0.9	
/XAO to output delay time	t _{PD4}	CL=200pF	- {	(-)	50	

Note: The measurement point for all of above signals is at 50% of input/output amplitude under Typical condition.

5.5 Optical specifications

Ta=25°C

Item		Symbol	Condition	Min	Тур.	Max	Unit	Remark
				40	50	-		
View Angle		θВ	CR≧10	60	70	-	Degree	Note 2
VIEW AIT	gie	θL	OK≡ IU	60	70	-	Degree	Note 2
		θR		60	70	-		
Contrast F	Ratio	CR	θ=0°	400	500	-	-	Note1
Response	Time	Ton	25℃		20	25	ms	Note1
rvesponse		T _{OFF}	230	-	20	20	1115	Note4
	White	х		0.260	0.310	0.360		Just CF,
	vviile	у		0.280	0.330	0.380		
	Red	х		0.540	0.590	0.640		
Color Filter	Neu	у	C-light	0.300	0.350	0.400		
Chromaticity	Green	х	C-light	0.298	0.348	0.398		polarizer
	Oreen	у		0.520	0.570	0.620		
	Blue	х		0.095	0.145	0.195		
	Blue	у		0.060	0.110	0.160		
NTSC	;	-	- 45 50 -		%	Note 5		
Transmitta	ance	L	-	3.8	4.1	-	%	Note1

Test Conditions:

- The ambient temperature is 25℃.
- 2. The test systems refer to Note 1 and Note 2.
- Viewing angle, contrast ratio and transmittance are test the panel include EWV polarizer (NWF-LNSW) with LED backlight.
- 4. The value of transmittance in this spec is a reference which will be affected by polarizer finally.

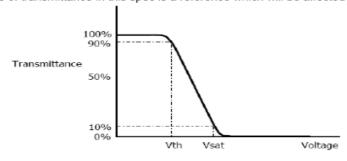
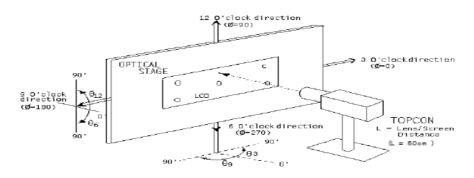


Figure 2. Measurement Set Up



5.6 LED back light specification (12 White Chips)

Item	Symbol	Condition	Min	Тур	Max	Unit
Forward Voltage	Vf	lf=40mA	18	19.2	20.4	V
Uniformity (with L/G)	ΔB _p	lf=40mA	80	-	-	%
Luminance for BL	L _V	If=40mA	4800	-	-	cd/m ²

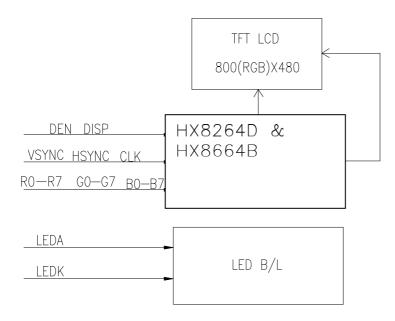
LED CIRCUIT

5.7 Interface PinConnections

PIN NO.	Symbol	Description					
1	LED-K	LED backlight(Cathode)					
2	LED-A	LED backlight(anode)					
3	GND	Ground					
4	VDD	Power supply (Digital +3.0V)					
5-12	R0-R7	Red Data					
13-20	G0-G7	Green Data					
21-28	B0-B7	Blue Data					
29	GND	Ground					
30	CLK	Clok (Latch data at clk falling edge)					
31	DISP	Display on/off					
32	HSYNC	Horizontal sync input in RGB mode (short to GND if not used)					
33	VSYNC	Vertical sync input in RGB mode (short to GND if not used)					
34	DEN	Data Enable					
35	NC	NC					
36	GND	Ground					
37	NC (XR)	NC(Touch panel coordinate in the right side of envisage drawing)					
38	NC (YD)	NC(Touch panel coordinate in the down side of envisage drawing)					
39	NC (XL)	NC(Touch panel coordinate in the left side of envisage drawing)					
40	NC (YU)	NC(Touch panel coordinate in the up side of envisage drawing)					

6 Signal timing diagram and Circuit block diagram

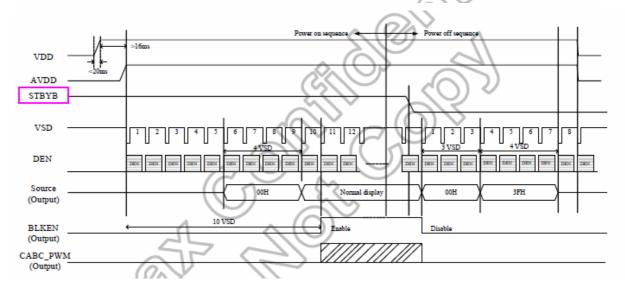
6.1 Circuit block diagram



6.2 Signal Timing Diagram

6.2.1 Power ON/OFF Sequence

HX8264-D02 has a power on/off sequence control function. In order to prevent IC from power on reset fail, the rising time (T POR) of the digital power supply VDD should be maintained within the given specifications. Please refer to "AC Characteristics" for more detail on timing.

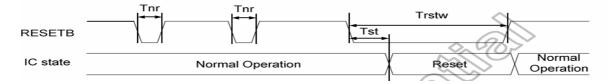


6.2.2 Reset timing

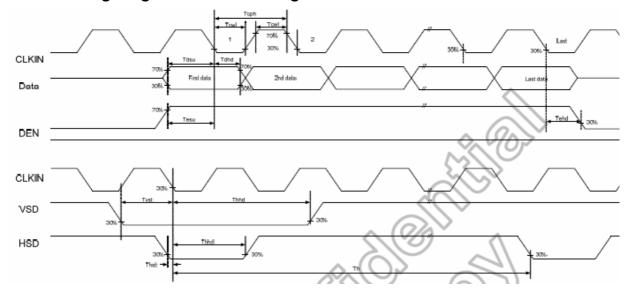
The reset input must be held for at least 1ms after power is stable.

Hardware reset timing

PARAMETER	Symbol		Unit		
PARAMETER	Syllibol	Min.	Тур.	Max.	Oilit
RESETB low pulse width	T_{rstw}	10	-	-	μs
Negative noise pulse width	T_{nr}		-	2	μs
Reset start time	T_{st}	2	-		μs

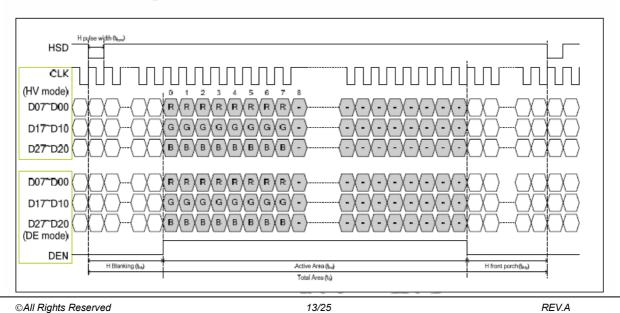


Timing Diagram of interface Signal

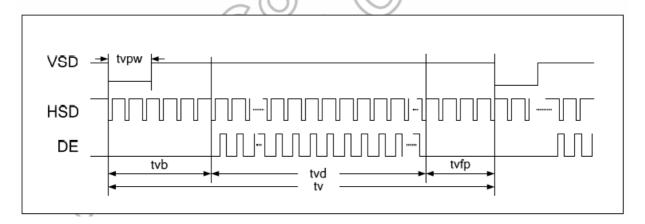


Parameter	Symbol		Unit		
Parameter	Symbol	Min.	Тур.	Max.	Unit
HS setup time	T _{hst}	8	-	-	ns
HS hold time	T _{hhd}	8	-	-	ns
VS setup time	T _{vst}	8	-	-	ns
VS hold time	T _{vhd}	8	-	- <	ns
Data setup time	T _{dsu}	8	-	-	ns
Data hold time	T _{dhd}	8	-	(0)	ns
DE setup time	T _{esu}	8	-	924/0) ns
DE hold time	T _{ehd}	8	-	Will	ns
VDD Power On Slew rate	T _{POR}	-	-	20	ms
RSTB pulse width	T _{Rst}	10	- ((us
CLKIN cycle time	T _{cph}	20	(/	V-	ns
CLKIN pulse duty	T _{cwh}	40	50	60	%
Output stable time	T _{sst}	-	((-2)	6	us

Horizontal timing



Vertical timing



Horizontal timing

Parameter	Symbol		Unit		
Farameter	Syllibol	Min.	Тур.	Max.	Onit
Horizontal Display Area	thd		800		DCLK
DCLK frequency	fclk	-	30	50	MHz
One Horizontal Line	th	889	928	1143	DCLK
HS pulse width	thpw	1	48	255 🗥	DCLK
HS Back Porch (Blanking)	thb		88		DCLK
HS Front Porch	thfp	1	40	255	DCLK
DE mode Blanking	th-thd	85	128	512	DCLK

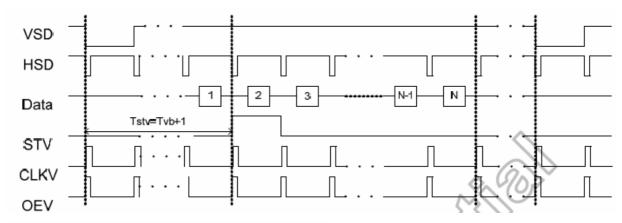
Vertical timing

Parameter	Symbol Spec.				Unit	
Parameter	Symbol	Min.	Тур.	Max.	Onit	
Vertical Display Area	tvd		480	~//	T _H	
VS period time	tv	513	525	767	T _H	
VS pulse width	tvpw	3	3	(255)) T _H	
VS Back Porch (Blanking)	tvb	5//	32		T _H	
VS Front Porch	tvfp		13	255	T _H	
DE mode Blanking	tv-tvd	4	45	255	T _H	

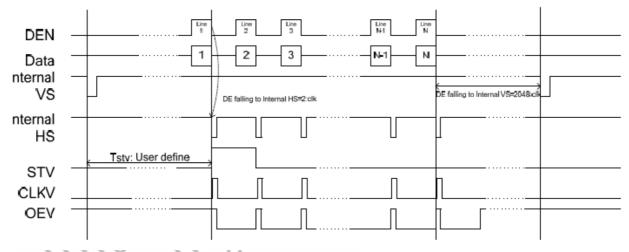
6.2.4 Driver Timing Control

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
CLKIN Frequency	Fclk	-	40	50	MHz	VDD=3.0V~3.6V
CLKIN Cycle Time	Tclk	20	25	-	ns	-
CLKIN Pulse Duty	Tcwh	40	50	60	%	Tclk
Time from HSD to Source Output	Thso		64		CLKIN	-
Time from HSD to LD	Thld		64		CLKIN	2.\\\ -
Time from HSD to STV	Thstv		2		CLKIN	(O) -
Time from HSD to CKV	Thckv		20		CLKIN	<u>-</u>
Time from HSD to OEV	Thoev		4		CLKIN	-
LD Pulse Width	Twld		10	1	CLKIN	-
CKV Pulse Width	Twckv		66		CLKIN	-
OEV Pulse Width	Twoev		74	(02)	CLKIN	-

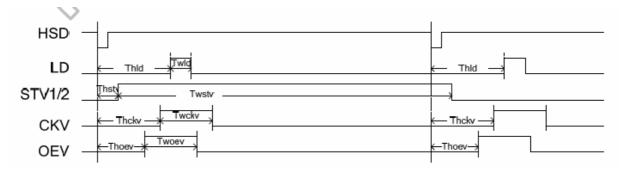
Vertical timing diagram HV (Cascade)



Vertical timing diagram DE (Cascade)



Gate output timing diagram (Cascade)



7 Reliability Test Conditions And Methods

NO	Item	Condition	Method
1	High / Low Temperature Storage	80℃/-30℃ 120hrs	Check and record every 48Hrs
2	High / Low Temperature Life	70℃/-20℃ 120hrs (operating mode)	Check and record every 48Hrs
3	High Temperature、 High Humidity Operating	60℃,90% RH, 96Hrs	Check and record every 48hrs
4	Thermal Shock	-30°C(30Min) → 25°C(5Min)	Each 10 cycles end , check
5	Vibration	10Hz~55Hz~10Hz Amplitude: 1.5mm 2hrs for each direction(X,Y,Z)	Each direction end, Check the Appearance and Electrical Characteristics
6	Static Electricity	Gap mood: ±1KV~±8KV (10 times air discharge with positive/negative voltage voltage gap : 1kv) Touch mood: ±1KV~±4KV	Each discharge end, Check the Electrical Characteristics
7	Curve	60 Thousand times, 40 times/min 150° (according to die if exist)	Check and record every 2~4 thousand times
8	Slump	Free faller movement for each side cording angle (75cm High 6 sides 2 angle 2 cording)	End

8 Inspection standard

No	Item	Criterion			
01	Outline Dimension	In accord with drawing			
02	Position-fin ding Dimension Assemble Dimension	In accord with drawing			
		Round type: non displa 3.1 Small area LCD	y Unit : mm		
		<u> </u>	Dimension	Qualified Quantity	
		$\rightarrow x \leftarrow \uparrow$	D≤0.1	Ignore	
			0.1 <d≤0.15< td=""><td>2</td></d≤0.15<>	2	
			D>0.15	0	
	LCDlack spots, white spots	3.2Large area LCD			
03	(Round type)	J.ZLarge area Lob	Dimension	Qualified Quantity	
		$\rightarrow x \leftarrow \uparrow$	D≤0.1	Ignore	
		•	0.1 <d≤0.15< td=""><td>2</td></d≤0.15<>	2	
			0.15 <d≤0.20< td=""><td>1</td></d≤0.20<>	1	
			D>0.20	0	
		C-STN : if D>0.1 , und	qualified		

		Unit : mm	4.1	Small	area LCD	
			Length	Width	Qualified Quantity	
			-	≤0.015	Ignore	
			≤1.0	0.015 <w≤< td=""><td>2</td></w≤<>	2	
			≤2.0	0.025	1	
			≤1.0	0.025 <w≤ 0.05</w≤ 	1	
	LCD)lack		-	D>0.05	According to circle	
04	spots, white spots (Line Style)		4.2Larg	ge area LCD		
		Style)	Length	Width	Qualified Quantity	
			w↑ L	-	≤0.015	Ignore
					₹ 2.0	0.015 <w≤ 0.025</w≤
				≤1.0	0.025 <w≤ 0.05</w≤ 	1
			-	D>0.05	According to circle	
	100			STN : If W ≥ 0.0 Ignore beyo	015 , unqualified nd viewing area	
05	Scratch 、 Threadlike Fiber	Same to NO.3 circle sightline and surface of LCD is vertical (2)Same to NO.3 line style				
06	POL	It is not admissible that POL is beyond the edge of glass, else, unqualified. It is essential that POL is over the 50 percent of width of frame, else, unqualified. According to the drawing in case of special definition.				
07	IC/FPC Bonding	Scratch		Reject		

		Intensity Of Adhesion	If lower than specification, reject
		Gold Fold Twist	Reject
07	IC/FPC	Silicon	According to outline, no gold outside, seal can not behigher than LCD
07	Bonding	FPC Gold Sever	Reject
08	SMT	Lack of Component Polarity Inverse	If exist, reject
		Leak Solder、 Virtual Solder	If exist, reject
		Short Circuit In Solder Point	If exist, reject
		Tin Ball	If exist, reject
		Tin Acumination	If visual, reject
		Height Solder Point	If higher 0.5mm than component. reject
		Height of component	Either side higher 0.5mm than component, reject

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		Component Shift	X Solder Pad component Y X<3/4Z y>1/3D reject
08	SMT	Few Tin	θ pad pad PCB If θ≤20° reject
		Component Deflection	Component Pad If Y >1/3D reject
		Component Carcass Sideways	Reject

		Component Carcass Sideways	If exist with visual inspection , reject	
		Lot Tin	A: Tin accrete the solder side completely , hollowly ,Ok B: Tin accrete the solder side completely , full circle arc , ok C: Jointing include whole solder side, height of tin>50 percent of height of component, reject	
		Few Tin	A: Tin accrete the solder side completely , hollowly ,Ok B: height of tin > 1/3 of solder side of component , ok C: height of tin ≤ 1/3 of solder side of component, reject	
08	SMT	Normal Jointing side		
		Short circuit 、 Open circuit	Forbid	
09	09 Light Quality of CSTN Display		1. Rolling strake with visual inspection, forbid 2. Differentness of color in viewing area with visual inspection (full white, red, green, blue), forbid 3. Display change with visual inspection, forbid	

		la !4 -	X	у	
		white	±0.05	±0.05	
	Color Of	Red	±0.05	±0.05	Drive LCD under normal condition, 25°C Φ=0 Θ=0
10	CIE	Green	±0.05	±0.05	Test white red green blue
	Coordinate	Blue	±0.05	±0.05	with DMS Roord
		According or samp approved		pecification mer have	
		In acco	od with	specification Measure to 3. Adjust to burrow ag press "mea display is s	ocation is in Follow Picture orightness instrument tozero, ainst the surface of LCD, easure", record when the
11	Brightness	In accor prod specific	uct		
					Measure location
12	CR (Max)	According to specification			ng to product specification re instrument (DMS-501)
13	Response time	Accord specific	-		ng to product specification re instrument (DMS-501)
14	Viewing angle	Accord specific	•		ng to product specification re instrument (DMS-501)
15	Vibration Ring	Compare sample c sup	ustomer	•	with the sample customer en assemble
16	Frequency Of FPC Bend	Accordin use of p (main F foldawa phone thousa	roduct FPC of ay cell F ≥6		Measure instrument Bend angle : 150° C in the casement when customer supply

9 Handling Precautions

9.1 Mounting method

The LCD panel of Daxian LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

9.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent [recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl), Salfur (S)

If goods were sent without being sili8con coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Salfur (S) from customer, Responsibility is on customer.

9.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

9.4 packing

- Module employ LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

9.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally

displayed but it resumes normal condition after turning off once.

• A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

9.6 storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
 [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

9.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

10 Precaution for use

10.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

10.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to Daxian, and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

11 Dimensional Outline

